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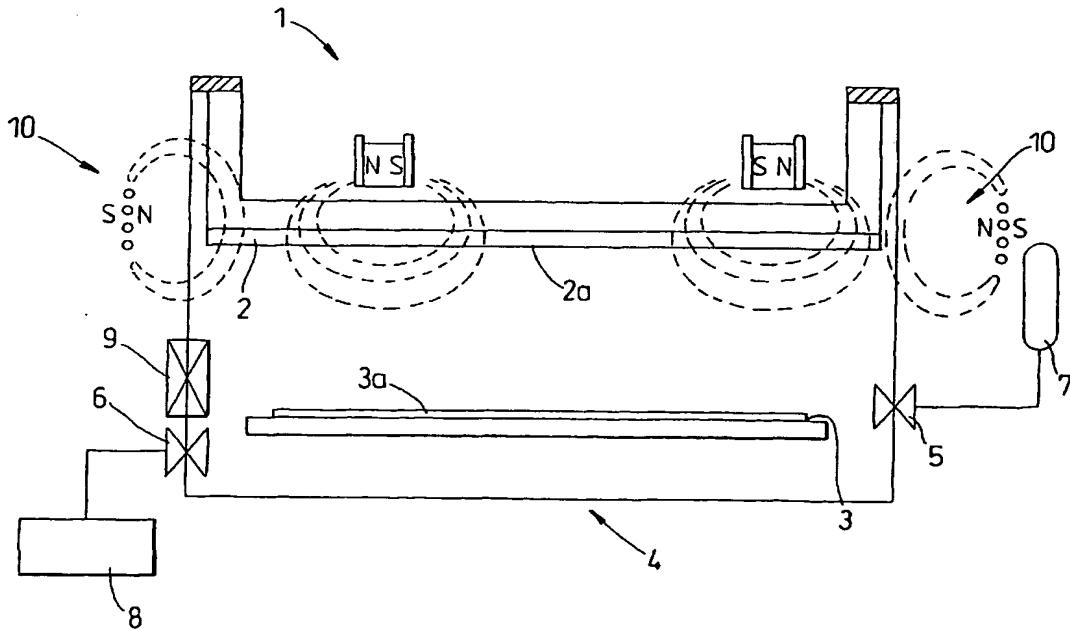
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(54) Title: METHODS OF SPUTTERING USING KRYPTON



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(57) Abstract: A method of sputtering a layer on a substrate having a plurality of recesses or openings includes using Krypton as a sputtering gas and is characterised in that the gas flow is less than 20 sccm and/or the Krypton pressure is less than 1 millitor.

METHODS OF SPUTTERING USING KRYPTON

This invention relates to methods of sputtering a layer on a substrate having a plurality of submicron sized recesses or openings.

As the dimensions of features on semiconductor devices, and other substrates, get progressively smaller, it becomes progressively more difficult to get effective coverage at the base of holes or recesses in the substrates when depositing sputtered layers. Quite a usual representation of degree of success is to plot the ratio of the thickness of the layer B deposited at the base of such a hole or recess against the thickness F of layer deposited on the field or upper surface of the substrate. There are various techniques that can be used to improve this ratio. One is to bias the substrate. The second is to include a collimator or to separate the target and substrate sufficiently for most of the atoms reaching the substrate to be travelling in a direction normal to the surface of the substrate. This is sometimes known as a "long throw" configuration. However, it is generally the case that if, by collimation or the use of a long throw configuration, one had ensured that the vast majority of atoms are travelling normal to the surface of the substrate when they reach the substrate.

A third technique is to ionise the sputtered material either by an ionising coil, or by using high power levels to the sputter target. These techniques may be used

Although the invention has been defined above it is to be understood that it includes any inventive combination of the features set out above or in the following description.

5 The invention may be performed in various ways and a specific embodiment will now be described, by way of example, with reference to the accompanying drawings, in which:

Figure 1 is a schematic view of an apparatus for performing a method of sputtering;

10 Figure 2 is a bar chart indicating the B/F ratio achieved for various sputtering conditions at the centre of the substrate; and

Figure 3 is the corresponding chart for features at the edge of the substrate.

15 In Figure 1, a target 2 and substrate support 3 are each contained within a vacuum low pressure vessel in the form of chamber 4 through which a gas can be streamed at low pressure via an inlet valve 5 and an outlet valve 6 from a respective gas source reservoir 7 and a vacuum pump 8. A substrate 3a can be placed on the substrate support 3 via a door 9. Plasma is confined by the coil assembly 10 thus enabling lower pressure operation at any given target voltage by lowering the plasma impedance. (A moving magnetron assembly 1 is associated with the target 2 that 20 is powered by a power supply 11. The wafer may be biased by power supply 12. A detailed explanation of the 25 operation of such a chamber is contained in our co-pending

edge, as shown in Figure 3, the base coverage produced by krypton, in a non bias set up, is an improvement on the argon case. At 0.24 millitorr pressure of Argon (the minimum that can be achieved with argon in the set up utilised) the B/F percentage had improved.

Switching to Krypton enables lower pressure operation ~ 0.15 millitorr was possible with the experimental target power supply. A considerably improved B/F percentage was achieved. This is not a predictable result. Theoretical calculations show that at 0.24 millitorr the mean free path of an Argon ion is 53cm - already comfortably exceeding the source to substrate distance of 43cm. No further improvement would therefore be expected from further reduced pressure operation. However at 0.15 millitorr, Krypton provides a significant improvement to the base coverage percentage. This lower pressure operation is most conveniently achieved by the use of Krypton as it enables lower voltage operation without special plasma ignition devices and/or high voltage power supplies that would be required for argon operation.

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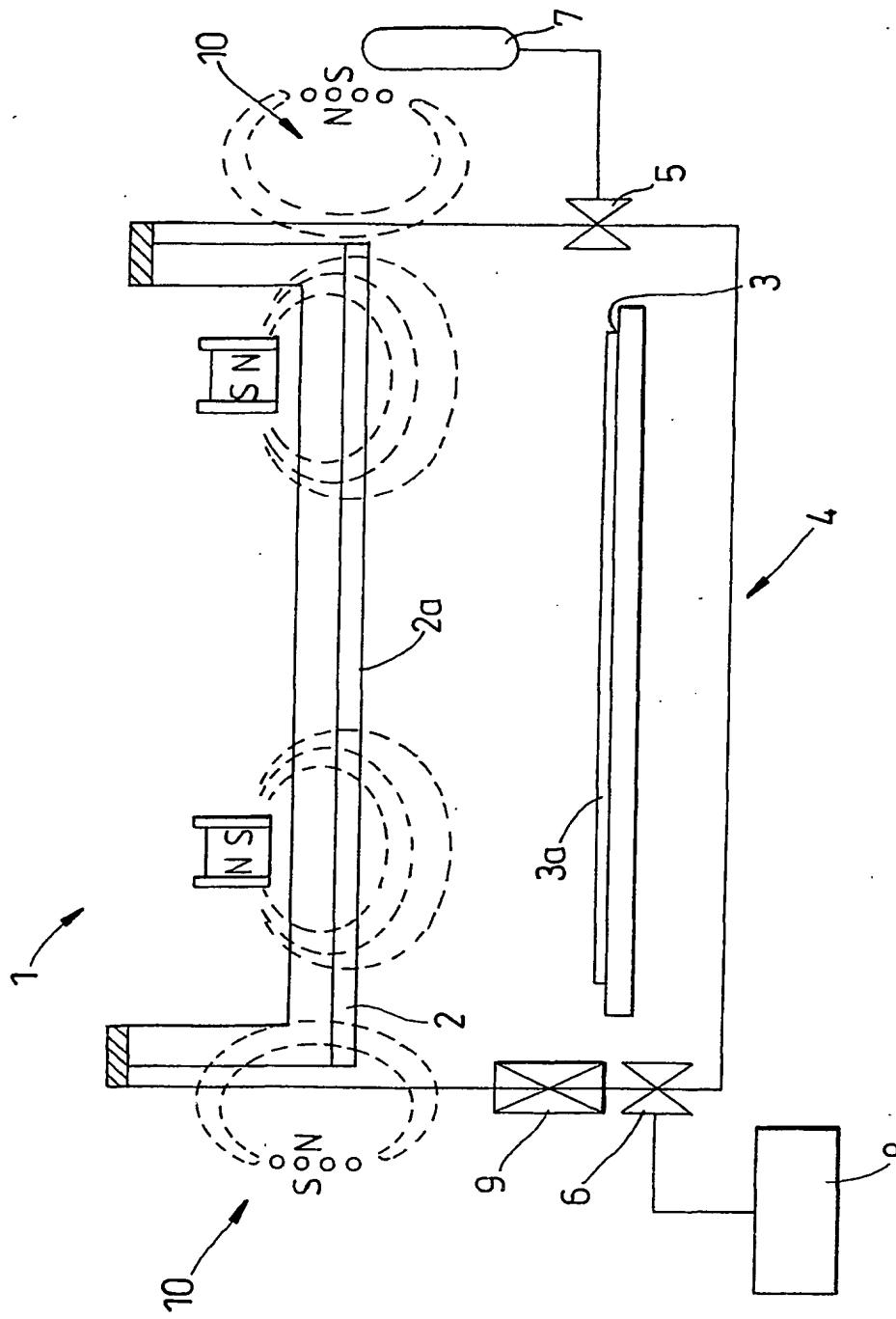


Fig. 1

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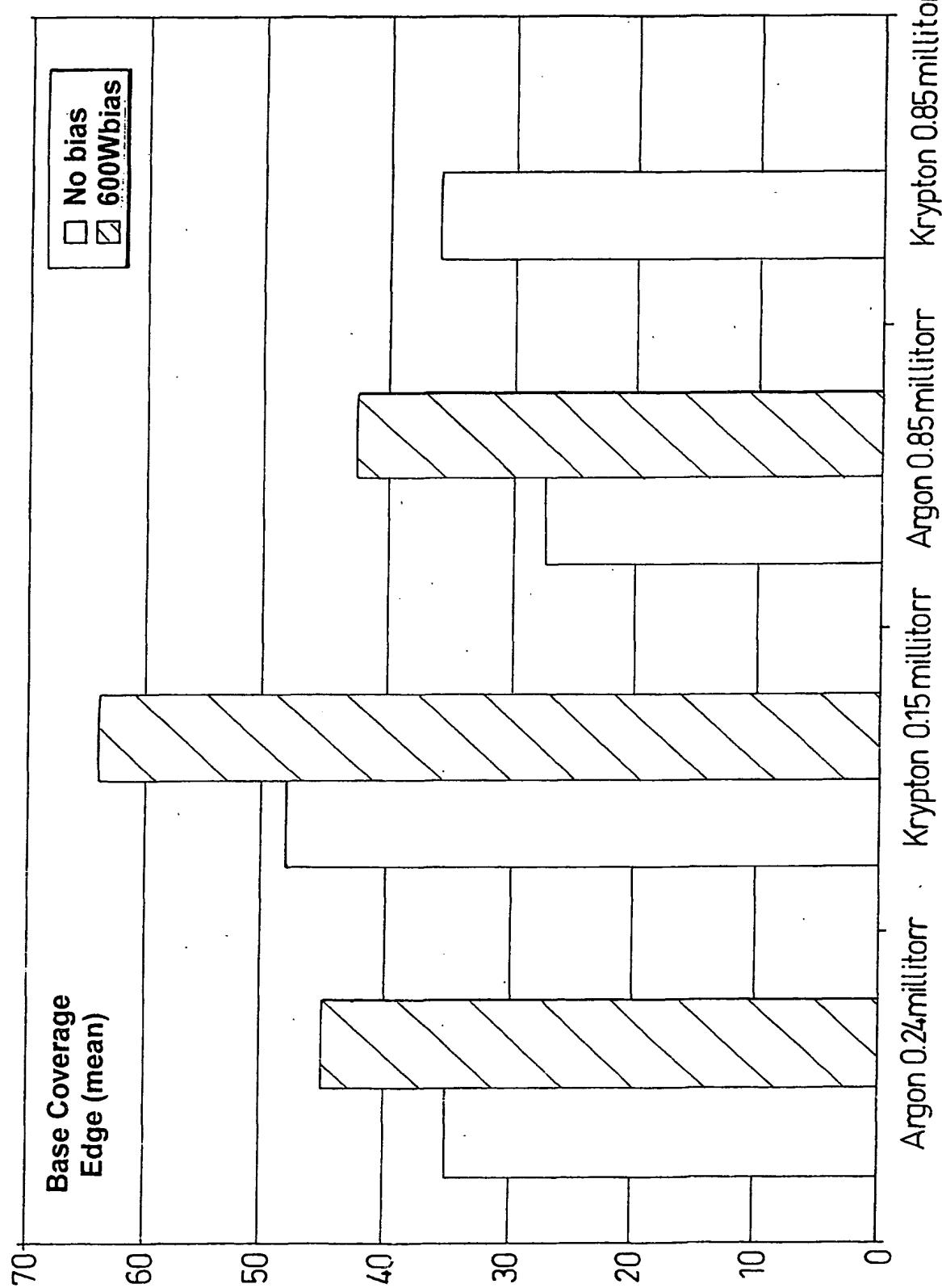


Fig. 3

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Patent document cited in search report	Publication date		Patent family member(s)	Publication date
EP 0846786	A	10-06-1998	EP 0846786 A2	10-06-1998
			JP 10219441 A	18-08-1998
			SG 53126 A1	28-09-1998
			TW 399101 B	21-07-2000
EP 0498663	A	12-08-1992	JP 2740591 B2	15-04-1998
			JP 5047708 A	26-02-1993
			DE 69206643 D1	25-01-1996
			DE 69206643 T2	05-06-1996
			EP 0498663 A2	12-08-1992
			KR 9504840 B1	13-05-1995
			US 5281554 A	25-01-1994